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IDS Flag Clearance for Application 09729443

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M844	08-26-2002	16	<input checked="" type="checkbox"/>	09-06-2002 20:37:59 pdeloatch
M844	10-23-2002	17	<input checked="" type="checkbox"/>	11-08-2002 18:00:27 rsherman
M844	11-13-2002	18	<input checked="" type="checkbox"/>	11-19-2002 15:57:24 rsherman
M844	04-17-2003	20	<input checked="" type="checkbox"/>	04-18-2003 20:41:13 rsherman
M844	05-05-2004	22	<input checked="" type="checkbox"/>	05-14-2004 21:20:03 rsherman
M844	01-09-2002	11	<input checked="" type="checkbox"/>	01-23-2002 07:39:13 achieves
M844	05-02-2002	12	<input checked="" type="checkbox"/>	05-09-2002 15:07:26 mgordon
M844	06-17-2002	13	<input checked="" type="checkbox"/>	07-02-2002 18:45:54 pdeloatch

UPDATE



Day : Tuesday  
Date: 4/25/2006  
Time: 09:51:49

## Inventor Information for 09/729443

Inventor Name	City	State/Country
JAFFE, STEVEN T.	IRVINE	CALIFORNIA
CAMERON, KELLY B.	IRVINE	CALIFORNIA
JONES, CHRISTOPHER R.	LOS ANGELES	CALIFORNIA

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Search Another: Application#

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Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



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## Inventor Name Search Result

Your Search was:

Last Name = JAFFE

First Name = STEVEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09430466</u>	<u>6553063</u>	150	10/29/1999	CONSTELLATION-MULTIPLEXED TRANSMITTER AND RECEIVER	JAFFE, STEVEN
<u>09729442</u>	<u>6693566</u>	150	12/04/2000	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN
<u>09739349</u>	Not Issued	93	12/15/2000	DIGITAL IF DEMODULATOR FOR VIDEO APPLICATIONS	JAFFE, STEVEN
<u>10045283</u>	Not Issued	61	10/18/2001	Constellation-multiplexed transmitter and receiver	JAFFE, STEVEN
<u>10109496</u>	Not Issued	61	03/28/2002	System and method for terrestrial high-definition television reception	JAFFE, STEVEN
<u>10184766</u>	Not Issued	61	06/28/2002	Quadrature receiver sampling architecture	JAFFE, STEVEN
<u>10184770</u>	Not Issued	30	06/28/2002	Sample rate reduction in data communication receivers	JAFFE, STEVEN
<u>10319929</u>	Not Issued	31	12/12/2002	Downstream time domain based adaptive modulation for DOCSIS based applications	JAFFE, STEVEN
<u>10372427</u>	Not Issued	30	02/21/2003	All digital radio frequency modulator	JAFFE, STEVEN
<u>10420089</u>	Not Issued	30	04/22/2003	PHY sub-channel processing	JAFFE, STEVEN
<u>10736434</u>	Not Issued	30	12/15/2003	Communications signal transcoder	JAFFE, STEVEN
<u>10902465</u>	Not Issued	93	07/30/2004	APPARATUS AND METHOD TO PROVIDE A LOCAL OSCILLATOR SIGNAL	JAFFE, STEVEN
<u>10902477</u>	Not Issued	30	07/30/2004	Apparatus and method for integration of tuner functions in a digital receiver	JAFFE, STEVEN
<u>10952171</u>	Not Issued	30	09/29/2004	Integrated burst FSK receiver	JAFFE, STEVEN
<u>11029789</u>	Not Issued	30	01/05/2005	Collapsible table	JAFFE, STEVEN
<u>29107212</u>	Not Issued	161	06/30/1999	TOY DISPLAY CONTAINER	JAFFE, STEVEN

<u>29107213</u>	Not Issued	161	06/30/1999	TOY DISPLAY CONTAINER	JAFFE, STEVEN
<u>29107214</u>	Not Issued	161	06/30/1999	TOY DISPLAY CONTAINER	JAFFE, STEVEN
<u>60106481</u>	Not Issued	159	10/30/1998	CONSTELLATION-MULTIPLEXING CODED MODULATION AND OPTIMUM RECEIVER	JAFFE, STEVEN
<u>60148801</u>	Not Issued	159	08/13/1999	DSL EQUALIZER	JAFFE, STEVEN
<u>60148978</u>	Not Issued	159	08/13/1999	DSL ADAPTIVE TOMLINSON ALGORITHMS	JAFFE, STEVEN
<u>60168808</u>	Not Issued	159	12/03/1999	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN
<u>60168809</u>	Not Issued	159	12/03/1999	VITERBI SLICER FOR TURBO CODES	JAFFE, STEVEN
<u>60171199</u>	Not Issued	159	12/15/1999	DIGITAL IF DEMODULATOR FOR VIDEO APPLICATIONS	JAFFE, STEVEN
<u>60230045</u>	Not Issued	159	09/01/2000	Satellite communication system	JAFFE, STEVEN
<u>60279500</u>	Not Issued	159	03/28/2001	System and method for terrestrial high-definition television reception	JAFFE, STEVEN
<u>60360467</u>	Not Issued	159	03/01/2002	All digital radio frequency modulator	JAFFE, STEVEN
<u>60381496</u>	Not Issued	159	05/17/2002	Sample rate reduction in data communication receivers	JAFFE, STEVEN
<u>60381497</u>	Not Issued	159	05/17/2002	Quadrature receiver sampling architecture	JAFFE, STEVEN
<u>60388987</u>	Not Issued	159	06/14/2002	PHY sub-channel processing	JAFFE, STEVEN
<u>60424205</u>	Not Issued	159	11/06/2002	Wireless headset having integrated headset processor	JAFFE, STEVEN
<u>60447112</u>	Not Issued	159	02/13/2003	Communications signal transcoder	JAFFE, STEVEN
<u>60516826</u>	Not Issued	159	11/03/2003	FEC (Forward Error Correction) decoder with dynamic parameters	JAFFE, STEVEN
<u>60534259</u>	Not Issued	159	01/05/2004	Portable table	JAFFE, STEVEN
<u>60569236</u>	Not Issued	159	05/10/2004	Integrated burst FSK receiver	JAFFE, STEVEN
<u>09490636</u>	<u>6529322</u>	150	01/24/2000	HIGH CONTRAST FRONT AND REAR VIEWING SURFACES FOR PROJECTION DISPLAYS	JAFFE, STEVEN M.
<u>09628490</u>	<u>6614161</u>	150	07/31/2000	RESONANT MICROCAVITY COMMUNICATION DEVICE	JAFFE, STEVEN M.
<u>09695630</u>	<u>6649432</u>	150	10/24/2000	RESONANT MICROCAVITY	JAFFE, STEVEN M.

				DISPLAY UTILIZING MIRRORS EXHIBITING ANOMALOUS PHASE DISPERSION	
<a href="#">09760284</a>	<a href="#">6392341</a>	150	01/12/2001	Resonant microcavity display with a light distribution element	JAFFE, STEVEN M.
<a href="#">09760398</a>	<a href="#">6404127</a>	150	01/12/2001	Multi-color microcavity resonant display	JAFFE, STEVEN M.
<a href="#">09855254</a>	<a href="#">6843590</a>	150	05/15/2001	WAVEGUIDE BASED LIGHT SOURCE	JAFFE, STEVEN M.
<a href="#">10391376</a>	<a href="#">7009211</a>	150	03/18/2003	RESONANT MICROCAVITY DISPLAY UTILIZING MIRRORS EXHIBITING ANOMALOUS PHASE DISPERSION	JAFFE, STEVEN M.
<a href="#">10443221</a>	Not Issued	30	05/22/2003	System and method for RMP printing	JAFFE, STEVEN M.
<a href="#">10651864</a>	Not Issued	93	08/29/2003	RESONANT MICROCAVITY COMMUNICATION DEVICE	JAFFE, STEVEN M.
<a href="#">10985681</a>	Not Issued	41	11/10/2004	Waveguide based light source	JAFFE, STEVEN M.
<a href="#">60161248</a>	Not Issued	159	10/25/1999	RESONANT MICROCAVITY DISPLAY UTILIZING MIRRORS EXHIBITING ANOMALOUS PHASE DISPERSION	JAFFE, STEVEN M.
<a href="#">60204645</a>	Not Issued	159	05/17/2000	Waveguide based light source	JAFFE, STEVEN M.
<a href="#">60385189</a>	Not Issued	159	05/30/2002	System and method for RMP printing	JAFFE, STEVEN M.
<a href="#">60774503</a>	Not Issued	20	02/16/2006	Microtiter plate photo reactor, thermo reactor and reader: quantitative- polymerase chain reaction illumination system	JAFFE, STEVEN M.
<a href="#">60774907</a>	Not Issued	20	02/16/2006	High thru-put micro capillary sequencer: capillary electrophoresis illumination system	JAFFE, STEVEN M.

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JAFFE	STEVEN	<input type="button" value="Search"/>

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## Inventor Name Search Result

Your Search was:

Last Name = JAFFE

First Name = STEVEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08094767</a>	<a href="#">5469018</a>	150	07/20/1993	RESONANT MICROCAVITY DISPLAY	JAFFE, STEVEN M.
<a href="#">08516944</a>	<a href="#">5616986</a>	150	08/18/1995	RESONANT MICROCAVITY DISPLAY	JAFFE, STEVEN M.
<a href="#">08581622</a>	<a href="#">5804919</a>	150	01/18/1996	RESONANT MICROCAVITY DISPLAY	JAFFE, STEVEN M.
<a href="#">08827189</a>	<a href="#">5955839</a>	150	03/26/1997	INCANDESCENT MICROCAVITY LIGHTSOURCE HAVING FILAMENT SPACED FROM REFLECTOR AT NODE OF WAVE EMITTED	JAFFE, STEVEN M.
<a href="#">09073711</a>	<a href="#">6198211</a>	150	05/06/1998	RESONANT MICROCAVITY DISPLAY	JAFFE, STEVEN M.
<a href="#">60117360</a>	Not Issued	159	01/27/1999	HIGH CONTRAST FRONT AND REAR VIEWING SURFACES FOR PROJECTION DISPLAYS	JAFFE, STEVEN M.
<a href="#">09433733</a>	<a href="#">6775334</a>	150	11/03/1999	EQUALIZATION AND DECISION-DIRECTED LOOPS WITH TRELLIS DEMODULATION IN HIGH DEFINITION TV	JAFFE, STEVEN T.
<a href="#">09550757</a>	Not Issued	41	04/17/2000	Decision feedback equalizer and precoder ramping circuit	JAFFE, STEVEN T.
<a href="#">09619125</a>	Not Issued	161	07/18/2000	Decision feedback equalizer and precoder ramping circuit	JAFFE, STEVEN T.
<a href="#">09685476</a>	<a href="#">6344871</a>	150	10/10/2000	NTSC interference rejection filter	JAFFE, STEVEN T.
<a href="#">09692805</a>	<a href="#">6411659</a>	150	10/20/2000	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
<a href="#">09729443</a>	Not Issued	80	12/04/2000	Viterbi slicer for turbo codes	JAFFE, STEVEN T.
<a href="#">09900222</a>	<a href="#">6518892</a>	150	07/06/2001	STOPPING CRITERIA FOR ITERATIVE DECODING	JAFFE, STEVEN T.
<a href="#">09946165</a>	Not Issued	83	09/04/2001	Satellite receiver	JAFFE, STEVEN T.

<u>10004515</u>	<u>6727936</u>	150	11/02/2001	NTSC INTERFERENCE REJECTION FILTER	JAFFE, STEVEN T.
<u>10150186</u>	<u>6771714</u>	150	05/16/2002	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
<u>10219858</u>	<u>6686853</u>	150	08/15/2002	METHOD AND APPARATUS FOR ITERATIVE DECODING	JAFFE, STEVEN T.
<u>10448062</u>	Not Issued	41	05/30/2003	Digital IF demodulator with carrier recovery	JAFFE, STEVEN T.
<u>10703286</u>	<u>6891485</u>	150	11/07/2003	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
<u>10751148</u>	<u>6982659</u>	150	01/02/2004	METHOD AND APPARATUS FOR ITERATIVE DECODING	JAFFE, STEVEN T.
<u>10776991</u>	Not Issued	30	02/12/2004	Digital IF demodulator	JAFFE, STEVEN T.
<u>10791686</u>	Not Issued	30	03/03/2004	Television functionality on a chip	JAFFE, STEVEN T.
<u>10822403</u>	<u>6828926</u>	150	04/12/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
<u>10826491</u>	Not Issued	30	04/16/2004	NTSC interference rejection filter	JAFFE, STEVEN T.
<u>10828450</u>	<u>6897791</u>	150	04/20/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
<u>10848907</u>	Not Issued	30	05/19/2004	Decision feedback equalizer and precoder ramping circuit	JAFFE, STEVEN T.
<u>10895579</u>	<u>7027528</u>	150	07/21/2004	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
<u>10895879</u>	Not Issued	61	07/21/2004	Equalization and decision-directed loops with trellis demodulation in high definition TV	JAFFE, STEVEN T.
<u>10916919</u>	Not Issued	30	08/12/2004	FEC (Forward Error Correction) decoder with dynamic parameters	JAFFE, STEVEN T.
<u>10976277</u>	<u>6985093</u>	150	10/28/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	JAFFE, STEVEN T.
<u>11031107</u>	Not Issued	30	01/07/2005	Dual mode QAM/VSB receiver	JAFFE, STEVEN T.
<u>11116851</u>	Not Issued	30	04/28/2005	Interspersed training among data	JAFFE, STEVEN T.
<u>60246425</u>	Not Issued	159	11/06/2000	Stopping criteria for decoding of turbo code	JAFFE, STEVEN T.
<u>07851019</u>	Not Issued	166	03/13/1992	IMAGE COMPRESSION ENCODING AND DECODING METHOD AND APPARATUS THEREFOR	JAFFE, STEVEN T.
<u>07983741</u>	<u>5398143</u>	150	12/01/1992	DATA PLACEMENT ON TAPE FOR	JAFFE, STEVEN T.

				A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	
<u>07983748</u>	Not Issued	166	12/01/1992	DATA CODING FOR A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	JAFFE, STEVEN T.
<u>08214153</u>	<u>5526131</u>	150	03/17/1994	DATA CODING FOR A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	JAFFE, STEVEN T.
<u>08239848</u>	<u>5533138</u>	150	05/09/1994	IMAGE COMPRESSION ENCODING AND DECODING METHOD AND APPARATUS THEREFOR	JAFFE, STEVEN T.
<u>08335211</u>	<u>5574565</u>	250	10/28/1994	DATA PLACEMENT ON TAPE FOR A DIGITAL VIDEO TAPE RECORDER SUITABLE FOR HIGH SPEED PICTURE PLAYBACK	JAFFE, STEVEN T.
<u>08406216</u>	<u>5588025</u>	150	03/15/1995	SINGLE OSCILLATOR COMPRESSED DIGITAL INFORMATION RECEIVER	JAFFE, STEVEN T.
<u>08472835</u>	<u>5635995</u>	150	06/07/1995	CHROMINANCE SIGNAL PROCESSOR FOR COMBINED TV/VCR SYSTEMS	JAFFE, STEVEN T.
<u>08541911</u>	<u>5619154</u>	150	10/10/1995	NUMERICAL VOLTAGE CONTROLLED OSCILLATOR	JAFFE, STEVEN T.
<u>08565608</u>	<u>5757855</u>	150	11/29/1995	DATA DETECTION FOR PARTIAL RESPONSE CHANNELS	JAFFE, STEVEN T.
<u>08602943</u>	<u>5872815</u>	150	02/16/1996	APPARATUS FOR GENERATING TIMING SIGNALS FOR A DIGITAL TELEVISION SIGNAL RECEIVER	JAFFE, STEVEN T.
<u>08702529</u>	<u>5894334</u>	150	09/05/1996	CARRIER RECOVERY SYSTEM FOR A VESTIGIAL SIDEBAND SIGNAL	JAFFE, STEVEN T.
<u>08704787</u>	<u>5805242</u>	150	09/06/1996	CARRIER INDEPENDENT TIMING RECOVERY SYSTEM FOR A VESTIGIAL SIDEBAND MODULATED SIGNAL	JAFFE, STEVEN T.
<u>08704788</u>	<u>5706057</u>	150	09/05/1996	PHASE DETECTOR IN A CARRIER RECOVERY NETWORK FOR A VESTIGIAL SIDEBAND SIGNAL	JAFFE, STEVEN T.
<u>08704789</u>	<u>5835532</u>	150	09/05/1996	BLIND EQUALIZER FOR A VESTIGIAL SIDEBAND SIGNAL	JAFFE, STEVEN T.
<u>08721663</u>	<u>5799037</u>	150	09/27/1996	RECEIVER CAPABLE OF DEMODULATING MULTIPLE DIGITAL MODULATION	JAFFE, STEVEN T.



				FORMATS	
<u>08721867</u>	<u>6005640</u>	150	09/27/1996	MULTIPLE MODULATION FORMAT TELEVISION SIGNAL RECEIVER SYSTEM	JAFFE, STEVEN T.

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Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



## Inventor Name Search Result

Your Search was:

Last Name = JAFFE

First Name = STEVEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09036925</a>	<a href="#">6169767</a>	150	03/09/1998	UNIVERSAL NETWORK INTERFACE MODULE	JAFFE, STEVEN T.
<a href="#">09303783</a>	<a href="#">6219088</a>	150	04/30/1999	NTSC INTERFERENCE REJECTION FILTER	JAFFE, STEVEN T.
<a href="#">09433730</a>	<a href="#">6842495</a>	150	11/03/1999	DUAL MODE QAM/VSB RECEIVER	JAFFE, STEVEN T.
<a href="#">09433734</a>	<a href="#">6222891</a>	150	11/03/1999	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
<a href="#">60040149</a>	Not Issued	159	03/10/1997	UNIVERSAL NETWORK INTERFACE MODULE	JAFFE, STEVEN T.
<a href="#">60106921</a>	Not Issued	159	11/03/1998	TIMING RECOVERY USING THE PILOT SIGNAL IN HIGH DEFINITION TV	JAFFE, STEVEN T.
<a href="#">60106923</a>	Not Issued	159	11/03/1998	EQUALIZATION AND DECISION-DIRECTED LOOPS WITH TRELLIS DEMODULATION IN HIGH DEFINITION TV	JAFFE, STEVEN T.
<a href="#">60106938</a>	Not Issued	159	11/03/1998	NTSC INTERFERENCE REJECTION FILTER	JAFFE, STEVEN T.
<a href="#">60107037</a>	Not Issued	159	11/03/1998	DUAL MODE QAM AND VSB RECEIVER	JAFFE, STEVEN T.
<a href="#">11197735</a>	Not Issued	71	08/02/2005	HDTV chip with a single if strip for handling analog and digital reception	JAFFE, STEVEN TODD
<a href="#">08919577</a>	Not Issued	161	02/20/1997	NUMERICAL VOLTAGE CONTROLLED OSCILLATOR	JAFFE, STEVEN TODD

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Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



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## Inventor Name Search Result

Your Search was:

Last Name = CAMERON

First Name = KELLY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07105401</a>	<a href="#">4873688</a>	150	10/05/1987	HIGH-SPEED REAL-TIME REED-SOLOMON DECODER	CAMERON, KELLY
<a href="#">07400447</a>	<a href="#">5099482</a>	150	08/30/1989	APPARATUS FOR DETECTING UNCORRECTABLE ERROR PATTERNS WHEN USING EUCLID'S ALGORITHM TO DECODE REED-SOLOMON (BCH) CODES	CAMERON, KELLY
<a href="#">07400480</a>	Not Issued	161	08/30/1989	SIMPLIFIED GALOIS FIELD LAMBDA'(X) CALCULATOR	CAMERON, KELLY
<a href="#">07400739</a>	<a href="#">5170399</a>	150	08/30/1989	REED-SOLOMON EUCLID ALGORITHM DECODER HAVING A PROCESS CONFIGURABLE EUCLID STACK	CAMERON, KELLY
<a href="#">07931826</a>	<a href="#">5339077</a>	150	08/18/1992	COMMA CODE GENERATOR	CAMERON, KELLY
<a href="#">08014319</a>	<a href="#">5406513</a>	150	02/05/1993	MECHANISM FOR PREVENTING RADIATION INDUCED LATCH-UP IN CMOS INTEGRATED CIRCUITS	CAMERON, KELLY
<a href="#">08332235</a>	<a href="#">5689452</a>	150	10/31/1994	METHOD AND APPARATUS FOR PERFORMING ARITHMETIC IN LARGE GALOIS FIELD GF(2 TO THE POWER OF N)	CAMERON, KELLY
<a href="#">09120894</a>	<a href="#">6133747</a>	250	07/22/1998	TIME REDUNDANT RADIATION TOLERANT LOGIC	CAMERON, KELLY
<a href="#">09430456</a>	<a href="#">6546520</a>	150	10/29/1999	GENERALIZED CONVOLUTIONAL INTERLEAVER/DEINTERLEAVER	CAMERON, KELLY
<a href="#">09951998</a>	<a href="#">6539516</a>	150	09/12/2001	FORWARD ERROR CORRECTOR	CAMERON, KELLY
<a href="#">10319929</a>	Not Issued	31	12/12/2002	Downstream time domian based adaptive modulation for DOCSIS based applications	CAMERON, KELLY
<a href="#">10325525</a>	<a href="#">6697975</a>	150	12/19/2002	GENERALIZED CONVOLUTIONAL INTERLEAVER/DEINTERLEAVER	CAMERON, KELLY
<a href="#">10382400</a>	<a href="#">6684364</a>	150	03/06/2003	FORWARD ERROR CORRECTOR	CAMERON, KELLY
<a href="#">10733823</a>	Not	93	12/11/2003	FORWARD ERROR CORRECTOR	CAMERON,

	Issued				KELLY
<u>10784114</u>	<u>7024597</u>	150	02/20/2004	GENERALIZED CONVOLUTIONAL INTERLEAVER/DEINTERLEAVER	CAMERON, KELLY
<u>60106482</u>	Not Issued	159	10/30/1998	EFFICIENT CONVOLUTIONAL INTERLEAVERS/DEINTERLEAVERS	CAMERON, KELLY
<u>60118725</u>	Not Issued	159	02/05/1999	UNGERBOECK DECODER ARCHITECTURE	CAMERON, KELLY
<u>60168808</u>	Not Issued	159	12/03/1999	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	CAMERON, KELLY
<u>60168809</u>	Not Issued	159	12/03/1999	VITERBI SLICER FOR TURBO CODES	CAMERON, KELLY
<u>60230045</u>	Not Issued	159	09/01/2000	Satellite communication system	CAMERON, KELLY
<u>60230578</u>	Not Issued	159	09/05/2000	Quasi error free (qef) communication using turbo codes	CAMERON, KELLY
<u>60232288</u>	Not Issued	159	09/12/2000	Parallel concatenated code with siso interactive turbo decoder	CAMERON, KELLY
<u>60424205</u>	Not Issued	159	11/06/2002	Wireless headset having integrated headset processor	CAMERON, KELLY
<u>09437448</u>	<u>6317858</u>	150	11/09/1999	FORWARD ERROR CORRECTOR	CAMERON, KELLY B.
<u>09498779</u>	<u>6741664</u>	150	02/05/2000	LOW-LATENCY HIGH-SPEED TRELLIS DECODER	CAMERON, KELLY B.
<u>09729442</u>	<u>6693566</u>	150	12/04/2000	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	CAMERON, KELLY B.
<u>09729443</u>	Not Issued	80	12/04/2000	Viterbi slicer for turbo codes	CAMERON, KELLY B.
<u>09808373</u>	Not Issued	161	03/13/2001	Apparatus for, and method of, receiving satellite television signals in an apartment building and providing television images in the receivers in such building	CAMERON, KELLY B.
<u>09878148</u>	Not Issued	71	06/08/2001	Parallel concatenated code with soft-in soft-out interactive turbo decoder	CAMERON, KELLY B.
<u>09900222</u>	<u>6518892</u>	150	07/06/2001	STOPPING CRITERIA FOR ITERATIVE DECODING	CAMERON, KELLY B.
<u>09947414</u>	Not Issued	41	09/05/2001	Quasi error free (QEF) communication using turbo codes	CAMERON, KELLY B.
<u>09952210</u>	<u>7023934</u>	150	09/12/2001	METHOD AND APPARATUS FOR MIN STAR CALCULATIONS IN A MAP DECODER	CAMERON, KELLY B.
<u>09952212</u>	Not Issued	71	09/12/2001	Method of normalization of forward metric (alpha) and reverse metric (beta) in a map decoder	CAMERON, KELLY B.
<u>09952309</u>	<u>7012975</u>	150	09/12/2001	METHOD AND APPARATUS FOR PERFORMING CALCULATIONS FOR FORWARD (ALPHA) AND REVERSE	CAMERON, KELLY B.

				(BETA) METRICS IN A MAP DECODER	
<a href="#">09952311</a>	Not Issued	95	09/12/2001	METHOD AND APPARATUS FOR PARALLEL DECODING OF TURBO ENCODED DATA	CAMERON, KELLY B.
<a href="#">09952312</a>	<a href="#">6940928</a>	150	09/12/2001	METHOD AND APPARATUS FOR SOFT-IN SOFT-OUT TURBO CODE DECODER	CAMERON, KELLY B.
<a href="#">09952455</a>	<a href="#">7035342</a>	150	09/12/2001	PARALLEL CONCATENATED CODE WITH SOFT-IN SOFT-OUT INTERACTIVE TURBO DECODER	CAMERON, KELLY B.
<a href="#">10219858</a>	<a href="#">6686853</a>	150	08/15/2002	METHOD AND APPARATUS FOR ITERATIVE DECODING	CAMERON, KELLY B.
<a href="#">10703286</a>	<a href="#">6891485</a>	150	11/07/2003	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	CAMERON, KELLY B.
<a href="#">10751148</a>	<a href="#">6982659</a>	150	01/02/2004	METHOD AND APPARATUS FOR ITERATIVE DECODING	CAMERON, KELLY B.
<a href="#">10822403</a>	<a href="#">6828926</a>	150	04/12/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	CAMERON, KELLY B.
<a href="#">10828450</a>	<a href="#">6897791</a>	150	04/20/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	CAMERON, KELLY B.
<a href="#">10829122</a>	<a href="#">6940929</a>	150	04/21/2004	LOW-LATENCY HIGH-SPEED TRELLIS DECODER	CAMERON, KELLY B.
<a href="#">10843606</a>	Not Issued	30	05/11/2004	Parallel concatenated code with soft-in soft-out interactive turbo decoder	CAMERON, KELLY B.
<a href="#">10843655</a>	Not Issued	83	05/11/2004	Parallel concatenated code with soft-in soft-out interactive turbo decoder	CAMERON, KELLY B.
<a href="#">10897200</a>	Not Issued	30	07/22/2004	Parallel concatenated code with soft-in soft-out interactive turbo decoder	CAMERON, KELLY B.
<a href="#">10897201</a>	Not Issued	30	07/22/2004	Parallel concatenated code with soft-in soft-out interactive turbo decoder	CAMERON, KELLY B.
<a href="#">10976277</a>	<a href="#">6985093</a>	150	10/28/2004	INTERSPERSED TRAINING FOR TURBO CODED MODULATION	CAMERON, KELLY B.
<a href="#">11116851</a>	Not Issued	30	04/28/2005	Interspersed training among data	CAMERON, KELLY B.
<a href="#">11150358</a>	Not Issued	20	06/10/2005	Low-latency high-speed trellis decoder	CAMERON, KELLY B.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">11284098</a>	Not Issued	19	11/21/2005	Apparatus for, and method of, receiving satellite television signals in an apartment building and providing television images in the receivers in such building	CAMERON, KELLY B.
<a href="#">60232053</a>	Not Issued	159	09/12/2000	Turbo trellis encoder and decoder	CAMERON, KELLY B.
<a href="#">06662747</a>	<a href="#">4629909</a>	150	10/19/1984	FLIP- FLOP FOR STORING DATA ON BOTH LEADING AND TRAILING EDGES OF CLOCK SIGNAL	CAMERON, KELLY B.
<a href="#">09056535</a>	Not Issued	161	04/08/1998	APPARATUS FOR, AND METHOD OF, RECEIVING SATELLITE TELEVISION SIGNALS IN AN APARTMENT BUILDING AND PROVIDING TELEVISION IMAGES IN THE RECEIVERS IN SUCH BUILDING	CAMERON, KELLY B.
<a href="#">09407232</a>	<a href="#">6326809</a>	150	09/27/1999	APPARATUS FOR AND METHOD OF ELIMINATING SINGLE EVENT UPSETS IN COMBINATIONAL LOGIC	CAMERON, KELLY B.
<a href="#">60053448</a>	Not Issued	159	07/22/1997	TIME REDUNDANT RADIATION TOLERANT LOGIC	CAMERON, KELLY B.
<a href="#">60107879</a>	Not Issued	159	11/09/1998	FORWARD ERROR CORRECTOR	CAMERON, KELLY B.
<a href="#">60246425</a>	Not Issued	159	11/06/2000	Stopping criteria for decoding of turbo code	CAMERON, KELLY B.
<a href="#">10264486</a>	Not Issued	71	10/04/2002	Variable code rate and signal constellation turbo trellis coded modulation codec	CAMERON, KELLY BRIAN
<a href="#">10264647</a>	Not Issued	95	10/04/2002	METRIC CALCULATION DESIGN FOR VARIABLE CODE RATE DECODING OF BROADBAND TRELLIS, TCM, OR TTCM	CAMERON, KELLY BRIAN
<a href="#">10264766</a>	<a href="#">7032164</a>	150	10/04/2002	EFFICIENT DESIGN TO CALCULATE EXTRINSIC	CAMERON, KELLY BRIAN

				INFORMATION FOR SOFT-IN-SOFT-OUT (SISO) DECODER	
<u>10264767</u>	Not Issued	95	10/04/2002	CLOSE TWO CONSTITUENT TRELLIS OF A TURBO ENCODER WITHIN THE INTERLEAVE BLOCK	CAMERON, KELLY BRIAN
<u>10335702</u>	Not Issued	61	01/02/2003	Single stage implementation of min*, max*, min and /or max to perform state metric calculation in SISO decoder	CAMERON, KELLY BRIAN
<u>10338185</u>	Not Issued	30	01/08/2003	Symbol by symbol variable code rate capable communication device	CAMERON, KELLY BRIAN
<u>10338376</u>	Not Issued	41	01/08/2003	Symbol by symbol variable constellation type and/or mapping capable communication device	CAMERON, KELLY BRIAN
<u>10338377</u>	Not Issued	89	01/08/2003	Communication decoder employing single trellis to support multiple code rates and/or multiple modulations	CAMERON, KELLY BRIAN
<u>10338432</u>	Not Issued	30	01/08/2003	Determination of variable code rates for a rate control sequence	CAMERON, KELLY BRIAN
<u>10338571</u>	Not Issued	71	01/08/2003	Parallel concatenated turbo code modulation encoder	CAMERON, KELLY BRIAN
<u>10347732</u>	Not Issued	30	01/21/2003	Inverse function of min*: min*-(inverse function of max*: max*-)	CAMERON, KELLY BRIAN
<u>10369168</u>	Not Issued	93	02/19/2003	LOW DENSITY PARITY CHECK (LDPC) CODE DECODER USING MIN*, MIN**, MAX* OR MAX** AND THEIR RESPECTIVE INVERSES	CAMERON, KELLY BRIAN
<u>10370136</u>	Not Issued	41	02/19/2003	Stopping and/or reducing oscillations in low density parity check (LDPC) decoding	CAMERON, KELLY BRIAN
<u>10407012</u>	Not Issued	30	04/03/2003	8 PSK rotationally invariant turbo trellis coded modulation without parallel transitions	CAMERON, KELLY BRIAN
<u>10444148</u>	Not Issued	89	05/22/2003	TTCM decoder design	CAMERON, KELLY BRIAN
<u>10446318</u>	Not Issued	30	05/27/2003	Non-systematic and non-linear PC-TCM (Parallel Concatenate Trellis coded modulation)	CAMERON, KELLY BRIAN
<u>10636008</u>	Not Issued	95	08/07/2003	16 QAM AND 16 APSK TTCM (TURBO TRELLIS CODED MODULATION) WITH MINIMUM BANDWIDTH EFFICIENCY OF 3 BIT/S/HZ USING A RATE 2/4 CONSTITUENT ENCODER	CAMERON, KELLY BRIAN
<u>10668526</u>	Not Issued	41	09/23/2003	LDPC (low density parity check) coded modulation symbol decoding	CAMERON, KELLY BRIAN

<u>10669066</u>	Not Issued	30	09/23/2003	Variable modulation with LDPC (low density parity check) coding	CAMERON, KELLY BRIAN
<u>10669177</u>	Not Issued	30	09/23/2003	Iterative metric updating when decoding LDPC (Low Density Parity Check) coded signals and LDPC coded modulation signals	CAMERON, KELLY BRIAN
<u>10718894</u>	<u>7032138</u>	150	11/21/2003	GENERALIZED CONVOLUTIONAL INTERLEAVER/DE-INTERLEAVER	CAMERON, KELLY BRIAN
<u>10723574</u>	Not Issued	30	11/26/2003	LDPC (Low Density Parity Check) coded modulation hybrid decoding	CAMERON, KELLY BRIAN
<u>10782142</u>	Not Issued	71	02/19/2004	Efficient LDPC code decoding with new minus operator in a finite precision radix system	CAMERON, KELLY BRIAN
<u>10802011</u>	Not Issued	20	03/16/2004	LDPC (Low Density Parity Check) coded modulation hybrid decoding using non-Gray code maps for improved performance	CAMERON, KELLY BRIAN
<u>10802023</u>	Not Issued	20	03/16/2004	LDPC (Low Density Parity Check) coded modulation symbol decoding using non-Gray code maps for improved performance	CAMERON, KELLY BRIAN
<u>10851614</u>	Not Issued	30	05/21/2004	LDPC (Low Density Parity Check) coded signal decoding using parallel and simultaneous bit node and check node processing	CAMERON, KELLY BRIAN
<u>10856023</u>	Not Issued	30	05/28/2004	WLAN transmitter having high data throughput	CAMERON, KELLY BRIAN
<u>10865456</u>	Not Issued	30	06/10/2004	Decoder design adaptable to decode coded signals using min* or max* processing	CAMERON, KELLY BRIAN
<u>10865556</u>	Not Issued	30	06/10/2004	Decoding LDPC (Low Density Parity Check) code and graphs using multiplication (or addition in log-domain) on both sides of bipartite graph	CAMERON, KELLY BRIAN
<u>10866542</u>	Not Issued	30	06/12/2004	Multi-dimensional space Gray code maps for multi-dimensional phase modulation as applied to LDPC (Low Density Parity Check) coded modulation	CAMERON, KELLY BRIAN
<u>10901528</u>	<u>7017106</u>	150	07/29/2004	LOW DENSITY PARITY CHECK (LDPC) CODE DECODER USING MIN*, MIN**, MAX* OR MAX** AND THEIR RESPECTIVE INVERSES	CAMERON, KELLY BRIAN
<u>10933586</u>	Not	30	09/03/2004	Multiple input multiple output	CAMERON, KELLY



	Issued			wireless local area network communications	BRIAN
<u>10979368</u>	Not Issued	30	11/01/2004	Asymmetrical mimo wireless communications	CAMERON, KELLY BRIAN
<u>11017087</u>	Not Issued	90	12/20/2004	BANDWIDTH EFFICIENT CODED MODULATION SCHEME BASED ON MLC (MULTI-LEVEL CODE) SIGNALS HAVING MULTIPLE MAPS	CAMERON, KELLY BRIAN
<u>11017157</u>	Not Issued	30	12/20/2004	IPHD (iterative parallel hybrid decoding) of various MLC (multi-level code) signals	CAMERON, KELLY BRIAN
<u>11017403</u>	Not Issued	30	12/20/2004	Decoding LDPC (low density parity check) code with new operators based on min* operator	CAMERON, KELLY BRIAN
<u>11171568</u>	Not Issued	20	06/30/2005	Common circuitry supporting both bit node and check node processing in LDPC (Low Density Parity Check) decoder	CAMERON, KELLY BRIAN
<u>11171569</u>	Not Issued	20	06/30/2005	Message passing memory and barrel shifter arrangement in LDPC (Low Density Parity Check) decoder supporting multiple LDPC codes	CAMERON, KELLY BRIAN
<u>11171727</u>	Not Issued	20	06/30/2005	Efficient front end memory arrangement to support parallel bit node and check node processing in LDPC (Low Density Parity Check) decoders	CAMERON, KELLY BRIAN
<u>11171998</u>	Not Issued	20	06/30/2005	Efficient design to implement LDPC (Low Density Parity Check) decoder	CAMERON, KELLY BRIAN
<u>11172165</u>	Not Issued	30	06/30/2005	Efficient design to implement min**/min**- or max**/max**- functions in LDPC (low density parity check) decoders	CAMERON, KELLY BRIAN
<u>11172329</u>	Not Issued	20	06/30/2005	Fast min* - or max* - circuit in LDPC (Low Density Parity Check) decoder	CAMERON, KELLY BRIAN

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">11172551</a>	Not Issued	30	06/30/2005	Decoding error correcting codes transmitted through multiple wire twisted pair cables with uneven noise on the wires	CAMERON, KELLY BRIAN
<a href="#">11190333</a>	Not Issued	20	07/27/2005	Construction of LDPC (Low Density Parity Check) codes using GRS (Generalized Reed-Solomon) code	CAMERON, KELLY BRIAN
<a href="#">11190334</a>	Not Issued	30	07/27/2005	Amplifying magnitude metric of received signals during iterative decoding of LDPC (Low Density Parity Check) code and LDPC coded modulation	CAMERON, KELLY BRIAN
<a href="#">11190657</a>	Not Issued	20	07/27/2005	Short length LDPC (Low Density Parity Check) code and modulation adapted for high speed Ethernet applications	CAMERON, KELLY BRIAN
<a href="#">11211210</a>	Not Issued	30	08/25/2005	LDPC (Low Density Parity Check) coded 128 DSQ (Double Square QAM) constellation modulation and associated labeling	CAMERON, KELLY BRIAN
<a href="#">11262574</a>	Not Issued	30	10/31/2005	AMP (accelerated message passing) decoder adapted for LDPC (low density parity check) codes	CAMERON, KELLY BRIAN
<a href="#">11264997</a>	Not Issued	30	11/02/2005	Construction of irregular LDPC (low density parity check) codes using RS (Reed-Solomon) codes or GRS (generalized Reed-Solomon) code	CAMERON, KELLY BRIAN
<a href="#">11264998</a>	Not Issued	30	11/02/2005	LDPC (Low Density Parity Check) coding and interleaving implemented in MIMO communication systems	CAMERON, KELLY BRIAN
<a href="#">11292133</a>	Not Issued	30	12/01/2005	System correcting random and/or burst errors using RS (Reed-Solomon) code, turbo/LDPC (Low Density Parity Check) code and convolutional interleave	CAMERON, KELLY BRIAN
<a href="#">11292134</a>	Not Issued	30	12/01/2005	System correcting random and/or burst errors using RS (Reed-Solomon)	CAMERON, KELLY BRIAN

				code, turbo/LDPC (Low Density Parity Check) code and convolutional interleave	
<u>11292135</u>	Not Issued	20	12/01/2005	Algebraic construction of LDPC (Low Density Parity Check) codes with corresponding parity check matrix having CSI (Cyclic Shifted Identity) sub-matrices	CAMERON, KELLY BRIAN
<u>11323901</u>	Not Issued	30	12/30/2005	Partial-parallel implementation of LDPC (low density parity check) decoders	CAMERON, KELLY BRIAN
<u>11360267</u>	Not Issued	19	02/23/2006	Sub-matrix-based implementation of LDPC (Low Density Parity Check ) decoder	CAMERON, KELLY BRIAN
<u>11360268</u>	Not Issued	19	02/23/2006	Implementation of LDPC (Low Density Parity Check) decoder by sweeping through sub-matrices	CAMERON, KELLY BRIAN
<u>60384464</u>	Not Issued	159	05/31/2002	Metric calculation design for variable code rate decoding of broadband trellis, TCM, or TTCM	CAMERON, KELLY BRIAN
<u>60384470</u>	Not Issued	159	05/31/2002	Close two constituent trellis of a turbo encoder within the interleave block	CAMERON, KELLY BRIAN
<u>60384698</u>	Not Issued	159	05/31/2002	Variable code rate and signal constellation turbo trellis coded modulation codec	CAMERON, KELLY BRIAN
<u>60384702</u>	Not Issued	159	05/31/2002	Efficient design to calculate extrinsic information for soft-in-soft-out (SISO) decoder	CAMERON, KELLY BRIAN
<u>60403847</u>	Not Issued	159	08/15/2002	Inverse of $\min^*$ : $\min^*$ -(the inverse of $\max^*$ : $\max^*$ -)	CAMERON, KELLY BRIAN
<u>60408978</u>	Not Issued	159	09/06/2002	Low density parity check (LDPC) code decoder using $\min^*$ , $\min^*$ -, $\min^{**}$ , and $\min^{**}$ -	CAMERON, KELLY BRIAN
<u>60427979</u>	Not Issued	159	11/20/2002	Single stage implementation of $\min^*$ , $\max^*$ , $\min$ and/or $\max$ to perform state metric calculations in SISO decoder	CAMERON, KELLY BRIAN
<u>60434734</u>	Not Issued	159	12/18/2002	TTCM decoder design	CAMERON, KELLY BRIAN
<u>60455734</u>	Not Issued	159	03/18/2003	8 PSK rotationally invariant turbo trellis coded modulation without parallel transitions	CAMERON, KELLY BRIAN
<u>60459132</u>	Not Issued	159	03/31/2003	True bit level decoding of TTCM (turbo trellis coded modulation) of variable rates and signal constellations	CAMERON, KELLY BRIAN
<u>60469777</u>	Not Issued	159	05/12/2003	Non-systematic and non-linear PC-TCM (Parallel Concatenate Trellis	CAMERON, KELLY BRIAN

				Coded Modulation)	
<u>60478690</u>	Not Issued	159	06/13/2003	Coded modulation with LDPC (low density parity check) code using variable maps and metric updating	CAMERON, KELLY BRIAN
<u>60480519</u>	Not Issued	159	06/20/2003	16 QAM and 16 APSK TCM (turbo trellis coded modulation) with minimum bandwidth of 3 bit/s/Hz using a rate 2/4 constituent encoder	CAMERON, KELLY BRIAN
<u>60490967</u>	Not Issued	159	07/29/2003	LDPC (low density parity check) coded modulation symbol decoding	CAMERON, KELLY BRIAN
<u>60519457</u>	Not Issued	159	11/12/2003	LDPC (low density parity check) coded modulation hybrid decoding	CAMERON, KELLY BRIAN
<u>60523272</u>	Not Issued	159	11/19/2003	Multi-dimensional space Gray maps for multi-dimensional phase modulation as applied to LDPC (Low Density Parity Check) coded modulation	CAMERON, KELLY BRIAN
<u>60535484</u>	Not Issued	159	01/10/2004	Decoding LDPC (Low Density Parity Check) code with new operators based on min* operator	CAMERON, KELLY BRIAN
<u>60535485</u>	Not Issued	159	01/10/2004	Bandwidth efficient coded modulation scheme based on multi-level code, multiple signal mapping, and IPHD (Iterative Parallel Hybrid Decoding)	CAMERON, KELLY BRIAN
<u>60541433</u>	Not Issued	159	02/03/2004	Efficient LDPC code decoding with new minus operator in a finite precision radix system	CAMERON, KELLY BRIAN
<u>60545854</u>	Not Issued	159	02/19/2004	WLAN transmitter having high data throughput	CAMERON, KELLY BRIAN
<u>60548953</u>	Not Issued	159	03/01/2004	LDPC (low density parity check) coded modulation symbol decoding using non-Gray code maps for improved performance	CAMERON, KELLY BRIAN
<u>60548971</u>	Not Issued	159	03/01/2004	LDPC (low density parity check) coded modulation hybrid decoding using non-Gray code maps for improved performance	CAMERON, KELLY BRIAN
<u>60556264</u>	Not Issued	159	03/25/2004	Multiple input multiple output wireless local area network communications	CAMERON, KELLY BRIAN
<u>60559701</u>	Not Issued	159	04/05/2004	LDPC (low density parity check) coded signal decoding using parallel and simultaneous bit node and check node processing	CAMERON, KELLY BRIAN
<u>60567571</u>	Not Issued	159	05/03/2004	Decoding LDPC ( Low Density Parity Check) code and graphs using multiplication (or addition in log-domain) on both sides of bipartite	CAMERON, KELLY BRIAN

				graph	
<a href="#"><u>60571655</u></a>	Not Issued	159	05/15/2004	Decoder design adaptable to decode coded signals using min* or max* processing	CAMERON, KELLY BRIAN
<a href="#"><u>60575920</u></a>	Not Issued	159	06/01/2004	Asymmetrical MIMO wireless communications	CAMERON, KELLY BRIAN
<a href="#"><u>60581876</u></a>	Not Issued	159	06/22/2004	Asymmetrical mimo wireless communications using STBC	CAMERON, KELLY BRIAN
<a href="#"><u>60602692</u></a>	Not Issued	159	08/19/2004	Short length LDPC (Low Density Parity Check) code and modulation adapted for high speed Ethernet applications	CAMERON, KELLY BRIAN
<a href="#"><u>60604426</u></a>	Not Issued	159	08/25/2004	Low-density parity check (LDPC) coded 128 double square QAM constellation modulation and its set-partition and gray code labeling	CAMERON, KELLY BRIAN
<a href="#"><u>60613923</u></a>	Not Issued	159	09/28/2004	Iterative decoding of low-density parity check (LDPC) code and LDPC coded modulation by altering initial metric	CAMERON, KELLY BRIAN
<a href="#"><u>60615722</u></a>	Not Issued	159	10/04/2004	Efficient design to implement LDPC (low density parity check) decoder	CAMERON, KELLY BRIAN
<a href="#"><u>60627452</u></a>	Not Issued	159	11/12/2004	Amplifying magnitude metric of received signal during iterative decoding of low-density parity check (LDPC) code and LDPC coded modulation	CAMERON, KELLY BRIAN
<a href="#"><u>60630360</u></a>	Not Issued	159	11/22/2004	Efficient design to implement min**or max** functions in LDPC (Low Density Parity Check) decoders	CAMERON, KELLY BRIAN
<a href="#"><u>60632237</u></a>	Not Issued	159	12/01/2004	Efficient front end memory arrangement to support parallel bit node and check node processing in LDPC (low density parity check) decoders	CAMERON, KELLY BRIAN
<a href="#"><u>60634923</u></a>	Not Issued	159	12/10/2004	Common circuitry supporting both bit node and check node processing in LDPC (Low Density Parity Check) decoder	CAMERON, KELLY BRIAN

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">60640579</a>	Not Issued	159	12/31/2004	Fast min*- or max*- circuit in LDPC (low density parity check) decoder	CAMERON, KELLY BRIAN
<a href="#">60641331</a>	Not Issued	159	01/04/2005	Message passing memory and barrel shifter arrangement in LDPC (low density parity check) decoder supporting multiple LDPC codes	CAMERON, KELLY BRIAN
<a href="#">60642689</a>	Not Issued	159	01/10/2005	Construction of LDPC (Low Density Parity Check) codes using generalized R-S (Reed-Solomon) code	CAMERON, KELLY BRIAN
<a href="#">60655312</a>	Not Issued	159	02/23/2005	Decoding error correcting codes transmitted through multiple wire twisted pair cables with uneven noise on the wires	CAMERON, KELLY BRIAN
<a href="#">60656566</a>	Not Issued	159	02/26/2005	AMP (Accelerated Message Passing) decoder adapted for LDPC (Low Density Parity Check) codes	CAMERON, KELLY BRIAN
<a href="#">60667408</a>	Not Issued	159	04/01/2005	System correcting random and/or burst errors using RS (Reed-Solomon) code, turbo/LDPC (Low Density Parity Check) code and convolutional interleave	CAMERON, KELLY BRIAN
<a href="#">60674084</a>	Not Issued	159	04/22/2005	Construction of irregular LDPC (Low Density Parity Check) codes using RS (Reed-Solomon) codes or GRS (Generalized Reed-Solomon) code	CAMERON, KELLY BRIAN
<a href="#">60675346</a>	Not Issued	20	04/27/2005	Construction of irregular LDPC (Low Density Parity Check) codes using RS (Reed-Solomon) codes or GRS (Generalized Reed-Solomon) code	CAMERON, KELLY BRIAN
<a href="#">60700127</a>	Not Issued	20	07/18/2005	Algebraic construction of LDPC (low density parity check) codes with corresponding parity check matrix having CSI (cyclic shifted identity) sub-matrices	CAMERON, KELLY BRIAN
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